

## ***NEWS RELEASE***

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### **Design Automation Conference to host 3rd UML for SoC Design Workshop July 23** *Yearly Event Held to Initiate Discussion About UML for Soc Design*

**BOULDER, Colo. □ June 21, 2006 □** The Third International Workshop for Unified Modeling Language™ (UML) for System-on-Chip (SoC) Design will be held Sunday, July 23, from 9 a.m.-6 p.m. at San Francisco's Moscone Center during the 43rd Design Automation Conference (DAC).

The workshop has been organized to coordinate efforts, to initiate discussion and to exchange experiences/information related to UML applied to SoC design and hardware aspects.

A keynote by Dr. Sreeranga Rajan, chair of the UML-SoC Profile Standardization Committee of the Object Management Group™ (OMG), will open the workshop and provide the latest information about a standard extension to UML for SoC development. Other presentations will explore embedded system analysis, design, modeling, synthesis and design flows supported by UML.

Product demonstrations from NEC, STMicroelectronics, the National University of Singapore, Philips and ARTiSAN Software will take place during the afternoon coffee break to highlight UML as a system-level visual language for specifying, analyzing, designing and verifying SoCs. A discussion to close the workshop will be moderated by

organizers John Wolfe of Mentor Graphics Corporation and Yves Vanderperren from Katholieke Universiteit Leuven in Belgium.

The workshop is open to anyone interested in learning more about UML for SoC Design. Registration is \$100 for members of ACM or IEEE and \$150 for non members.

For more information regarding registration and the complete program, visit the DAC Website at <http://www.dac.com> or the UML workshop Website at <http://www.c-lab.de/uml-soc>

DAC will be held Monday, July 24, through Friday, July 28, at the Moscone Center. Details are found at: <http://www.dac.com>.

### **About DAC**

DAC is the premier forum for the electronic design industry to exchange information on products, methodologies and processes. Attended by more than 10,000 developers, designers, researchers, managers and engineers from leading electronics companies and universities around the world, DAC includes more than 250 exhibitors and offers a robust technical program covering the electronics industry's hottest trends to bring people to the event.

The conference is sponsored by the Association for Computing Machinery's Special Interest Group on Design Automation (ACM/SIGDA), the Circuits and Systems Society and Computer Aided Network Design Technical Committee of the Institute of Electrical and Electronics Engineers (IEEE/CASS/CANDE), and the Electronic Design Automation Consortium (EDA Consortium). More details about DAC are available at: [www.dac.com](http://www.dac.com).

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