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News Release

For more information, contact:
Public Relations for DAC:
Kara Udziela, Weber Shandwick
(503) 552-3731
kudziela@webershandwick.com

**Industry Leaders to Keynote 43rd Design Automation Conference with
Emphasis on Structuring Process and Design for Mobile Communications and the
Challenges of Convergence**

*Dr. Hans Stork of Texas Instruments to Deliver Tuesday's Keynote and
Dr. Alessandro Cremonesi of STMicroelectronics to Present Thursday's Keynote*

BOULDER, Colo., April 24, 2006 □ The Design Automation Conference (DAC), the electronic design automation (EDA) industry's premier event, today announced that Dr. Hans Stork, Senior Vice President, Chief Technology Officer and Director of Silicon Technology Development for Texas Instruments, will deliver the Tuesday keynote address on July 25. On Thursday, July 27, Dr. Alessandro Cremonesi, Strategy and System Technology Group Vice President and Advanced System Technology General Manager for STMicroelectronics, will present an additional keynote address. The 43rd annual DAC will be held in San Francisco, Calif., at the Moscone Center, July 24-28, 2006.

On Tuesday, July 25, Dr. Stork will present a keynote address titled, "Structuring Process and Design for Future Mobile Communication Devices." Dr. Stork will demonstrate how such devices are a driving force for continued economies of scaling in design automation. He will discuss the density and speed of sub-50 nm CMOS technology that is enabling mobile communication device design, as well as the process variations, power issues and complexity that require improved modeling of systematic manufacturing variations and design sensitivities.

On Thursday, July 27, Dr. Cremonesi will deliver “The Challenges of Convergence.” Dr. Cremonesi’s theme centers on the challenges the semiconductor industry will have to face to address the new trends and opportunities in major application fields in this era of convergence. From the platform architecture perspective, where most of these applications will run, multiprocessing is already a reality and the industry will have to find new paradigms to handle the increased complexity at the system, embedded software, and at the silicon implementation level. Dr. Cremonesi will conclude with future perspectives from the viewpoint of ST's advanced research organization.

“From the process technology level to the system-on-a-chip level, both Dr. Stork and Dr. Cremonesi offer great depth of experience in mobile communications and are working on important projects in this arena at two of our industry’s most cutting-edge companies,” said Ellen Sentovich, general chair of the 43rd annual DAC. “They are well-qualified to bring DAC attendees great insight into the challenges to tackle as the proliferation of multimedia devices brings more opportunities and demands to our industry.”

Keynote Speaker Biographies

Dr. Stork is Senior Vice President, and Chief Technology Officer, of Texas Instruments. As Director of the Silicon Technology Development organization, he is responsible for ensuring that process technology provides a competitive advantage for TI’s products.

Prior to joining Texas Instruments in 2001, Dr. Stork was Director of the ULSI Research Lab and later the Internet Systems and Storage Lab at HP Laboratories, Hewlett-Packard. Dr. Stork started his professional career at IBM's T.J. Watson Research Center, working on

advanced bipolar technology and circuits, and his group demonstrated the early successes of SiGe HBTs.

Dr. Stork serves on the Board of Directors for International Sematech and the Semiconductor Research Corporation (SRC). He also serves on the Governing Councils of the Focus Center Research Programs and Nanotechnology Research Initiative. He is a member of the SIA Technology Strategy Committee.

Alessandro Cremonesi received a Doctorate in Electronics Engineering from the University of Pavia, Italy, in 1984. After a period of research activity in the opto-electronics field at the University of Pavia, he joined STMicroelectronics working in different fields, including telecommunications, audio/video digital signal processing and multimedia applications. At present, Dr. Cremonesi is Vice President of the Strategy and System Technology Group and General Manager of the Advanced System Technology (AST) group at STMicroelectronics, with the responsibility of corporate system research and development and corporate strategic marketing activities across 14 different STMicroelectronics Labs worldwide.

About DAC

DAC is the premier forum for the electronic design industry to exchange information on products, methodologies and processes. Attended by more than 10,000 developers, designers, researchers, managers and engineers from leading electronics companies and universities around the world, DAC includes more than 240 exhibitors and offers a robust technical program covering the electronics industry's hottest trends.

The conference is sponsored by the Association for Computing Machinery's Special Interest Group on Design Automation (ACM/SIGDA), the Circuits and Systems Society and Computer Aided Network Design Technical Committee of the Institute of Electrical and Electronics

Engineers (IEEE/CASS/CANDE), and the Electronic Design Automation Consortium (EDA Consortium). More details about DAC are available at: www.dac.com.

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