

47th DAC DEMOGRAPHICS



JUNE 13-18, 2010

Conference Attendance	1638
Exhibit Only/Free Monday Attendees	1464
Total Attendance	3102
Booth Staff (exhibitors)	2417
Total Participants	5519

47th DAC Registration Questionnaire Results

1) In which industry segment does your group or organization work?		
101	System Design - System	10.8%
102	System Design - Board	1.5%
103	System Design - Hardware	17.7%
104	System Design - Software	3.8%
105	EDA Software	22.5%
106	IP/Core Design	11.7%
107	Foundry/IC Manufacturing	5.6%
108	Design Services: Includes ASIC Services & Post Silicon Services	6.6%
109	University/Research Institution	16.2%
110	Financial	1.1%
111	Publishing/Media/Industry Analyst	2.1%
112	Other	0.2%

4) What primary end product or service do you work on?		
401	Automotive Electronics	2.0%
402	Avionics, Military, Government	3.7%
403	Communication Systems (Wired or Wireless)	11.7%
404	Components (Passives)	0.4%
405	Computers, Systems, & Peripherals	8.3%
406	Consumer Electronics	8.2%
407	Medical Equipment	1.1%
408	Design Services/Consultant	4.4%
409	EDA Software	20.9%
410	Embedded Systems	4.4%
411	Research/Education	13.1%
412	Semiconductors	16.7%
413	Library Models/IP/Cores	1.6%
414	Media/Publishing	1.9%
415	Other	1.5%

2) What is your principal job function?		
201	Executive/Senior Management	15.7%
202	Engineering Management	14.2%
203	CAD Engineering Management	4.9%
204	CAD Engineer	9.1%
205	Hardware Logic Design Engineer	7.2%
206	Hardware Physical Design Engineer	4.8%
207	Verification Engineer	3.7%
208	Application Engineer	3.1%
209	Embedded Software Engineer	0.8%
210	Application Software Engineer	1.4%
211	Board Design & Layout	0.8%
212	Test & Assembly	0.5%
213	System Architect	2.1%
214	Professor/Researcher	11.8%
215	Student	11.0%
216	Financial or Industry Analyst	0.7%
217	Venture Capitalist	0.3%
218	Editor/Publisher	0.8%
219	Marketing/Sales	5.8%
220	Other	1.2%

5) Which products and/or services in the following areas do you recommend, purchase or influence the purchase of?		
501	Analog/MS Design Simulation	21.8%
502	Digital Logic Simulation	29.4%
503	Physical Verification	20.4%
504	Other Logic Verification	12.4%
505	Logic Design and Synthesis	27.1%
506	Packaging	6.4%
507	PCB	7.9%
508	Placement & Routing	18.7%
509	Full-Custom Layout	13.5%
510	Test	7.8%
511	RTL & Formal Verification	24.3%
512	Equivalence Checking	14.7%
513	Design for Manufacturing & Yield	14.8%
514	Algorithm Design	11.6%
515	System Design and Specification	15.0%
516	Software Design	12.8%
517	HW/SW Codesign	15.9%
518	Foundry/Silicon Vendor	7.9%
519	Application Specific Std. Part (ASSP)	4.1%
520	System/Logic Design	12.8%
521	Physical Design	15.5%
522	Verification	16.0%
523	Post Silicon Services	5.6%
524	Libraries/Models	11.6%
525	IP/Cores	15.8%
526	None	20.9%

3) What type of design are you involved in?		
301	Analog ICs/Mixed Signal ICs	25.3%
302	Analog/Mixed Signal Systems	13.6%
303	ASIC	40.2%
304	Application Specific Std. Part (ASSP)	7.3%
305	Digital ICs	34.2%
306	Digital Systems	19.1%
307	DSPs	7.5%
308	RTOS	4.2%
309	Firmware	7.7%
310	Middleware	3.3%
311	Application	9.0%
312	Embedded Systems	19.6%
313	MEMS	3.4%
314	Memory	9.6%
315	Microprocessor/Microcontroller Design	11.1%
316	PCB Layout	6.1%
317	PLDs/FPGAs	13.0%
318	RF/Microwave	5.8%
319	Specialized Processors (GPU, NPU, etc.)	5.9%
320	Library Models/IP/Cores	10.7%
321	Not Directly Involved in Design	15.2%
322	Other	2.1%

6) Which tool flow do you predominately use?		
601	Custom/Structured Custom	29.1%
602	COT	9.7%
603	ASIC	36.8%
604	Structured ASIC	2.6%
605	FPGA	18.0%
606	Other	3.7%