

Date	Time	Room(s)	Event	Topic Area
Sunday, June 8	8:00-9:00	Collocated MEMOCODE 2008 - Thursday	Collocated MEMOCODE 2008 - Thursday	Business
	9:00-10:00	209A	Workshop: High-Level Synthesis: Back to the Future - Rooms: 209A Meeting, 209B Lunch	Synthesis and FPGA
	10:00-11:00	206B	Workshop: 5th International UML for SoC Design Workshop: UML in Application - Rooms: 209A Meeting, 209B Lunch	Synthesis and FPGA
	11:00-12:00	204C	Collocated 6th IEEE Symposium on Application Specific Processors - Rooms: 204C Meeting, 204B Lunch	Synthesis and FPGA
	12:00-1:00	203A	Workshop: System and SoC Debug - Rooms: 203A Meeting, 203B Lunch	Synthesis and FPGA
Monday, June 9	8:00-9:00	206B	Collocated 6th IEEE Symposium on Application Specific Processors - Rooms: 204C Meeting, 204B Lunch	Synthesis and FPGA
	9:00-10:00	212A	Workshop: Cross-Layer Power and Thermal Management - Rooms: 212A Meeting, 212B Lunch	Synthesis and FPGA
	10:00-11:00	205A	Workshop: Women in Design Automation: Networking, Negotiation, and Nonsense: Achieving Career Balance in an Unbalanced World - Rooms: 205A Meeting, 205B Lunch	Business
	11:00-12:00	207D	Workshop: Diagnostic Services in Network-on-Chips (DSNOC) - Test, Debug, and On-line Monitoring - Rooms: 207D Meeting, 207C Lunch	Synthesis and FPGA
	12:00-1:00	204A	Collocated IEEE International Workshop on Hardware-Oriented Security and Trust (HOST-2008) - Rooms: 204A Meeting, 204B Lunch	Synthesis and FPGA
Tuesday, June 10	8:00-9:00	206A	Collocated 6th IEEE Symposium on Application Specific Processors - Rooms: 204C Meeting, 204B Lunch	Synthesis and FPGA
	9:00-10:00	207A	Workshop: Introduction to Chips and EDA for a Non-technical Audience	Synthesis and FPGA
	10:00-11:00	208B	Workshop: Integrated Design Systems Workshop: DAC 2008 - OpenAccess: A Platform for Continuous Evolution and Innovation - Rooms: 208B Meeting, 208A Lunch	Synthesis and FPGA
	11:00-12:00	209A	Workshop: Beyond Syntax and Semantics: Industry Experiences with OVL/SVA/PSL	Synthesis and FPGA
	12:00-1:00	210A	Workshop: Gary Smith on EDA Trends and What's Hot at DAC	Synthesis and FPGA
Wednesday, June 11	8:00-9:00	206A	Collocated 6th IEEE Symposium on Application Specific Processors - Rooms: 204C Meeting, 204B Lunch	Synthesis and FPGA
	9:00-10:00	207A	Workshop: Introduction to Chips and EDA for a Non-technical Audience	Synthesis and FPGA
	10:00-11:00	208B	Workshop: Integrated Design Systems Workshop: DAC 2008 - OpenAccess: A Platform for Continuous Evolution and Innovation - Rooms: 208B Meeting, 208A Lunch	Synthesis and FPGA
	11:00-12:00	209A	Workshop: Beyond Syntax and Semantics: Industry Experiences with OVL/SVA/PSL	Synthesis and FPGA
	12:00-1:00	210A	Workshop: Gary Smith on EDA Trends and What's Hot at DAC	Synthesis and FPGA
Thursday, June 12	8:00-9:00	206A	Collocated 6th IEEE Symposium on Application Specific Processors - Rooms: 204C Meeting, 204B Lunch	Synthesis and FPGA
	9:00-10:00	207A	Workshop: Introduction to Chips and EDA for a Non-technical Audience	Synthesis and FPGA
	10:00-11:00	208B	Workshop: Integrated Design Systems Workshop: DAC 2008 - OpenAccess: A Platform for Continuous Evolution and Innovation - Rooms: 208B Meeting, 208A Lunch	Synthesis and FPGA
	11:00-12:00	209A	Workshop: Beyond Syntax and Semantics: Industry Experiences with OVL/SVA/PSL	Synthesis and FPGA
	12:00-1:00	210A	Workshop: Gary Smith on EDA Trends and What's Hot at DAC	Synthesis and FPGA
Friday, June 13	8:00-9:00	206A	Collocated 6th IEEE Symposium on Application Specific Processors - Rooms: 204C Meeting, 204B Lunch	Synthesis and FPGA
	9:00-10:00	207A	Workshop: Introduction to Chips and EDA for a Non-technical Audience	Synthesis and FPGA
	10:00-11:00	208B	Workshop: Integrated Design Systems Workshop: DAC 2008 - OpenAccess: A Platform for Continuous Evolution and Innovation - Rooms: 208B Meeting, 208A Lunch	Synthesis and FPGA
	11:00-12:00	209A	Workshop: Beyond Syntax and Semantics: Industry Experiences with OVL/SVA/PSL	Synthesis and FPGA
	12:00-1:00	210A	Workshop: Gary Smith on EDA Trends and What's Hot at DAC	Synthesis and FPGA

Topic Area Legend:

- Synthesis and FPGA
- Business
- Interconnect and Reliability
- Analog/Mixed-signal/RF and Simulation
- Verification and Test
- Physical Design
- New and Emerging Technologies
- DFM and the Manufacturing Interface
- Wireless
- Strictly Design
- System-level and Embedded
- Low Power Design

Management Day - Tuesday, June 10
10:30am-6:00pm

The Management Day is designed to provide managers with timely information to help them make decisions where business and technology intersect. The day is comprised of three sessions featuring presentations by managers representing key independent device manufacturers (IDMs) and major fab companies.

Consumer application chips are the technology drivers today. They require different types of optimizations and thus the adoption of emerging solutions to meet such requirements. Optimizing for high volume production, low power, and shrinking sizes necessitates adequate trade-off analysis and technical/business decision-making

by management. Also, moving to new semiconductor technology nodes, such as 45nm and 65nm, can significantly affect the choices of suppliers.

The Management Day sessions will discuss the changing needs and demonstrate corresponding management decision criteria to make the right choices from a pool of alternate options for flows, methodologies and suppliers. The leading managers of today's most complex nanometer chips will present these emerging solutions and their economic impact. This year's Management Day will provide a unique opportunity for managers to gain insights from their peers in the industry.

Wireless Theme at DAC

Wireless applications can be found in every aspect of our lives. The wireless communications industry is diverse and ranges from systems like cellular phones to personal wireless internet like WiFi, to shorter range devices like low-power Bluetooth devices.

DAC explores wireless design and its impact on design automation from both a business and a technical perspective. While wireless communications systems represented more than 67% of the total communications systems market in 2007,

cellular technology was more than 40% of that wireless market and remains the single dominant element to drive design automation technology.

Wireless semiconductor systems design requires design success on numerous fronts. This makes DAC the ideal place for the industry to explore design automation advances to solve wireless design challenges. DAC covers the pressing deep submicron, complex SoC design, verification, low-power and hardware/software design challenges that must be resolved to support continued productivity of wireless design teams.

DAC Workshops

- DAC has an exciting lineup of workshops and collocated events... nearly four times as many as in previous years. This year's schedule includes:
- Sunday, June 8**
 - 8:30am-5:15pm High-level Synthesis: Back to the Future
 - 9:00am-6:00pm 5th International UML for SoC Design Workshop - UML in Application
 - 9:00am-4:30pm Biochips to Interface and Monitor Human Biological Functions
 - 9:00am-5:15pm System and SoC Debug Workshop
 - 2:00-5:00pm Design and Verification of Low Power SoCs: An Application Oriented Approach
 - 4:00-7:00pm Low Power Coalition Workshop - Advances in Low Power Design for Circuits and Systems
 - Monday, June 9**
 - 8:30am-5:30pm Cross-layer Power and Thermal Management
 - 9:00am-5:00pm Diagnostic Services in Network-on-Chips (DSNOC)
 - 10:00am-12:00pm Introduction to Chips and EDA for a Non-technical Audience
 - 9:00am-1:45pm Workshop for Women in Design Automation (WWINDA): Networking, Negotiation, and Nonsense: Achieving Career Balance in an Unbalanced World
 - 12:00-4:00pm 4th Integrated Design Systems Workshop: DAC 2008
 - 1:00-5:15pm Beyond Syntax and Semantics: Industry Experiences with OVL/SVA/PSL

- Tuesday, June 10**
 - 10:00am-12:00pm Effective Technical Writing
 - iDesign Track Tuesday Keynote, Sessions 1, 7 and 13
- Thursday, June 12**
 - 2:00-4:00pm Maximizing Efficiency in the Development Cycle
- Adjunct Event**
 - Global Semiconductor Test Consortium Conference (GSC)
 - Breaker Verification Systems
 - Calypso Design Systems
 - Carbon Design Systems, Inc.
 - CAST, Inc.
 - Cebaltech, Inc.
 - Certess, Inc.
 - ChipEstimate.com
 - ChipVision Design Systems, Inc.
 - Ciranova, Inc.
 - CloSoft, Inc.
 - CLK Design Automation, Inc.
 - CMF
 - CoFluent Design
 - Common Platform
 - Concept Engineering GmbH
 - Coupling Wave Solutions
 - CoWare, Inc.
 - CriticalBlue
 - Cyber Semiconductor, Inc.
 - DAC Pavilion
 - DAFA, Inc.
 - Dassault Systemes ENOVA Corp.
 - Dataream
 - DATE 09
 - Derail Software, Inc.
 - Design and Reuse

Exhibition

Visit the DAC exhibition to learn in-depth information on new products and services from 250 vendors offering products for all phases of the electronic design process including EDA tools, IP cores, embedded system and system-level tools, as well as design-for-manufacturing, silicon vendors and design services companies.

The DAC show floor features its unique exhibit booth and private suite combination, which gives you the freedom to deeply explore the products on the show floor and find the right solution for your design flow. Find out how you can improve performance and shorten the time-to-market on your next design.

Check out these features of the DAC exhibition:
New Best of DAC contest! Look for kiosks on the floor and vote for your favorite vendors! When you cast your ballot you will be entered to win a Nintendo Wii!

NEW Exhibitor Forum - DAC's newest feature for exhibit floor education. Attendees are invited to the Exhibitor Forum in Booth 2849 to hear a series of technical presentations from exhibitors on focused topics. Compare how exhibitors are solving your tough design issues with their latest tools and methodologies.

DAC Pavilion - The popular DAC Pavilion is back in Booth 364 with 20 presentations this year on business and technical issues.

New FREE Exhibits-only Pass. DAC is offering a new registration option to attendees. Register for the Exhibits-only pass by May 19 and receive entrance to all days of the exhibition, three keynotes, DAC Pavilion sessions and the new Exhibitor Forum at no charge. Don't hesitate, after May 19 the cost is \$65 - still a terrific value!

Exhibition Hours
Monday-Wednesday, June 9-11 9:00am-6:00pm
Thursday, June 12 9:00am-1:00pm

- EXHIBITING COMPANIES**
- Accellon Technologies, Inc.
 - ACIT - New Systems Research
 - ACE Associated Compiler Experts by Agilent Technologies
 - Aldec, Inc.
 - Altos Design Automation
 - Analog Bits Inc.
 - Analog Rails
 - Anchor Semiconductor, Inc.
 - Ansoft Corp.
 - Apache Design Solutions, Inc.
 - Applied Simulation Technology
 - Artwork Conversion Software, Inc.
 - Atotech
 - Atlanta Inc.
 - Attachmate
 - astriummicrosystems USA, Inc.
 - AutoES, Design Technologies Inc.
 - Averant, Inc.
 - Avery Design Systems, Inc.
 - Axiom Design Automation
 - Azuro, Inc.
 - Beach Solutions Ltd.
 - Berkeley Design Automation, Inc.
 - Blaze DFM, Inc.
 - Blue Pearl Software
 - Bluespec, Inc.
 - Breaker Verification Systems
 - Calypso Design Systems
 - Carbon Design Systems, Inc.
 - CAST, Inc.
 - Cebaltech, Inc.
 - Certess, Inc.
 - ChipEstimate.com
 - ChipVision Design Systems, Inc.
 - Ciranova, Inc.
 - CloSoft, Inc.
 - CLK Design Automation, Inc.
 - CMF
 - CoFluent Design
 - Common Platform
 - Concept Engineering GmbH
 - Coupling Wave Solutions
 - CoWare, Inc.
 - CriticalBlue
 - Cyber Semiconductor, Inc.
 - DAC Pavilion
 - DAFA, Inc.
 - Dassault Systemes ENOVA Corp.
 - Dataream
 - DATE 09
 - Derail Software, Inc.
 - Design and Reuse
 - Dini Group (The)
 - Dolphin Integration
 - Dorado Design Automation, Inc.
 - Doulos
 - DTIC, National Tsing Hua University
 - Duolog Technologies Ltd.
 - Dynalith Systems Co., Ltd.
 - EDWACT
 - Elevier
 - EMA Design Automation, Inc.
 - Entasys Design, Inc.
 - Envision Technology
 - Europractice
 - EVE
 - Exhibitor Forum
 - Extreme DA
 - Fenix Design Automation
 - FishTail Design Automation
 - Forto Design Systems
 - FTL Systems, Inc.
 - *GAIAC
 - *Gauda
 - Geneys Testware, Inc.
 - GIDEL
 - Gradient Design Automation
 - Handshake Solutions
 - Helix S.A.
 - Hewlett-Packard Co.
 - Hummingbird - An Open Text Connectivity Solutions Group
 - IBM Corp.
 - IC Manage, Inc.
 - IEEE Media
 - IHS
 - Impresso, Inc.
 - Inceneta Design Systems, Inc.
 - Infinscale
 - Innovative Silicon Inc.
 - Intel Corp.
 - IntermetCAD.com, Inc./Timberwolf Systems
 - Interra Systems, Inc.
 - Jasper Design Automation, Inc.
 - Javelin Design Automation, Inc.
 - JEDA Technologies
 - *Jopeed Design Automation, Inc.
 - KEI / IP SoC Support Center
 - Kivapak Technology, Inc.
 - Laflin Institute
 - Legend Design Technology, Inc.
 - Library Technologies, Inc.
 - Liga Systems, Inc.
 - LogicVision, Inc.
 - Lorentz Solution
 - Lyngnet, Inc.
 - Magillem Design Services
 - Magma Design Automation, Inc.
 - MathWorks, Inc. (The)
 - Mentor Graphics Corp.
 - Micro Magic, Inc.
 - Micrologic Design Automation
 - Mibel, Inc.
 - MOSIS Service (The)
 - ManuEDA GmbH
 - MyCAD, Inc.
 - Navigate
 - Nanum Technologies, Inc.
 - Nascentric, Inc.
 - National Instruments Corp.
 - NEC Informatec Systems, Ltd.
 - Novas Software, Inc.
 - Novelics
 - Nusym Technology, Inc.
 - OCP International Partnership
 - OEA International, Inc.
 - OneSpin Solutions GmbH
 - Open IT, Inc.
 - OPTEM Engineering Inc.
 - Paradigm Works, Inc.
 - Peetra
 - Physware, Inc.
 - Platform Computing
 - *Polar Instruments Inc.
 - *POUTEDA Software Corp.
 - Prentice Hall Professional / Pearson Education, Inc.
 - ProDesign Electronic Corp.
 - Prolific, Inc.
 - Pysis Technology, Inc.
 - QTHink
 - Real Intent, Inc.
 - Reed Business Information
 - Runtime Design Automation
 - Sagantec
 - Samsung
 - Samtec, Inc.
 - Samtiff Europe
 - *Satin IP Technologies
 - Semifore, Inc.
 - Sequence Design, Inc.
 - Shearwater Group, Inc.
 - SIGDA/DAC University Booth
 - Signity, Inc.
 - Silicon Canas Inc.
 - Silicon Design Solutions
 - Silicon Frontline Technology
 - Silicon Image
 - Silicon Integration Initiative - S2, Inc.
 - Silicon Navigator
 - Simucad Design Automation, Inc.
 - *Snowkesh Microelectronics
 - SoftIn Technologies Pvt. Ltd.
 - Solido Design Automation, Inc.
 - *Sonnet Software, Inc.
 - *Springer
 - StarNet Communications
 - Sun Microsystems
 - Swati Design
 - *Synapse Design Automation
 - SynCra
 - Symfona, Inc.
 - Synopsys, Inc.
 - Symplify, Inc.
 - Symtest Technologies, Inc.
 - Takumi Technology Corp.
 - Tanner EDA
 - Target Compiler Technologies
 - TechForce, Inc.
 - Teklatech
 - *Tela Innovations
 - Tensilica, Inc.
 - Time To Market Inc.
 - TOOL Corp.
 - True Circuits, Inc.
 - TSMC
 - TSSI - Test Systems Strategies, Inc.
 - Tuscany Design Automation, Inc.
 - UMC
 - Unifly
 - VeriZ Solutions, Inc.
 - Verific Design Automation
 - Veritools, Inc.
 - WASK, Inc.
 - Virage Logic Corp.
 - *VirtuLogic, Inc.
 - Xoomsys
 - XVALIS
 - Z Circuit Automation, Inc.
 - Zeland Software, Inc.
 - (as of March 26, 2008)
 - * Indicates first time exhibitor

